ABSTRACT

A liquid crystal display device that uses in-plane switching is capable of eliminating a stain generated at an outer area of a thin film transistor array and minimizing a time delay of a common voltage applied to a thin film transistor array. In the device, a plurality of data lines apply data signals to a thin film transistor array. A plurality of gate lines apply gate signals to the thin film transistor array. A plurality of common voltage lines apply a common voltage to the thin film transistor array. The common voltage lines are provided at the outer area of the thin film transistor array and are spaced from the thin film transistor array by a distance of more than 1 to 1.5mm. A plurality of dummy signal lines are arranged in parallel to the common voltage lines and adjacent to the common voltage lines provided at the outer area of the thin film transistor array to apply alternating current signals.